

#### **STK6711AMK4**

# 4-Phase Stepping Motor Driver Unipolar Self-Excitation Type (I<sub>O</sub>=1.5A)

#### Overview

The STK6711AMK4 is a unipolar fixed-current choppertype self-excitation 4-phase stepping motor driver hybrid IC which uses MOSFET power devices. The excitation sequence signal is active-high.

#### **Applications**

- Serial printer, line printer, and laser beam printer (LBP) paper feed and carriage motor drivers.
- PPC scanner and LBP feed drivers.

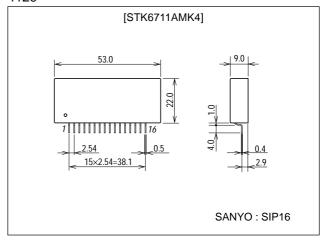
#### **Features**

- Simultaneous ON prevention circuit built-in to prevent driver breakdown due to simultaneous ON control signals from the control system's microcontroller, etc.
- Self-excitation design means chopping frequency is determined by motor L and R. Supports chopping at 20kHz or higher.
- Two  $0.33\Omega$  current detection resistors built-in for fixed-current chopping operation.
- Wide operating supply voltage range (18 to 42V for motor drive).
- Unipolar design enables use as a driver for hybrid, PM, or VR-type stepping motors
- Supports W1-2 phase operation, with dual Vref pins.

#### **Package Dimensions**

unit:mm

4129



#### **Series Organization**

The following devices from a series with differing excitation signal active level and output capacity. Some of the following devices are under development. Contact your Sanyo sales representative if you require more detailed information.

Type No.	Excitation signal	Output current (per phase)	Туре		
STK6711AMK4	Active-high	1.5A			
STK6711BMK4	Active-low	1.5A			
STK6712AMK4	Active-high	1.7A	Self-		
STK6712BMK4	Active-low	1.7A	excitation		
STK6713AMK4	Active-high	3.0A			
STK6713BMK4	Active-low	3.0A			
STK6714AMK4	Active-high 4.0A		Fixed		
STK6714BMK4	Active-low	4.0A	excitation		

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# **Specifications**

## **Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage 1	V <sub>CC</sub> 1 max	No input signal	52	V
Maximum supply voltage 2	V <sub>CC</sub> 2 max	No input signal	7	V
Maximum phase current	I <sub>OH</sub> max	R=5Ω, L=10mH, 1×0.5s pulse, V <sub>CC</sub> applied	2.2	Α
Repeated avalanche handling capability	Ear max		38	mJ
Junction temperature	Tj max		150	°C
Operating substrate temperature	Tc max		105	°C
Storage temperature	Tstg		-40 to +125	°C

#### Allowable Operating Ranges at Ta = 25°C

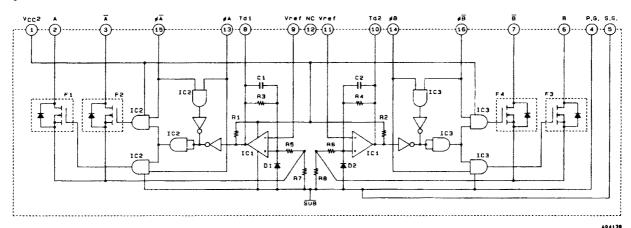
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offit
Supply voltage 1	V <sub>CC</sub> 1	With input signal	18		42	V
Supply voltage 2	V <sub>CC</sub> <sup>2</sup>	With input signal	4.75	5.00	5.25	V
Phase driver withstand voltage	V <sub>DSS</sub>		100			V
Phase current	I <sub>OH</sub> max	50% duty			1.5	Α

## **Electrical Characteristics** at Ta = 25 °C, $V_{CC}1=36V$ , $V_{CC}2=5V$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Output saturation voltage	Vst	R <sub>L</sub> =23Ω, Vin=2.4V		1.4	1.9	V
Average output current	loave	Load : R=3.5Ω, L=3.8mH, Vin=2.4V, per phase	0.45	0.50	0.55	Α
Pin current drain	I <sub>CC</sub> 2	Load : R=3.5Ω, L=3.8mH, Vin=2.4V, per phase		15	25	mA
FET diode forward voltage	Vdf	If=1.0A		1.2	1.8	V
TTL-input high level voltage	V <sub>IH</sub>	Input voltage when F1, F2, F3, F4 ON	2.0			V
TTL-input low level voltage	V <sub>IL</sub>	Input voltage when F1, F2, F3, F4 OFF			0.8	V
Switching time	<sup>t</sup> ON	R <sub>L</sub> =24Ω, Vin=2.4V		50		ns
	t <sub>OFF</sub>	$R_L=24\Omega$ , Vin=2.4V		0.1		μs

Note: All tests are made using a constant-voltage supply.

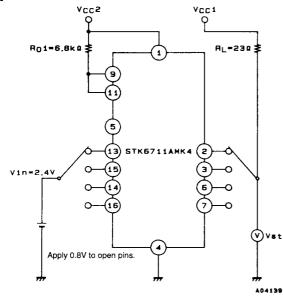
# **Equivalent Circuit**

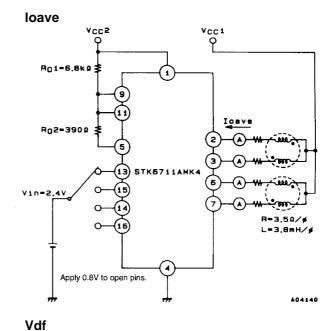


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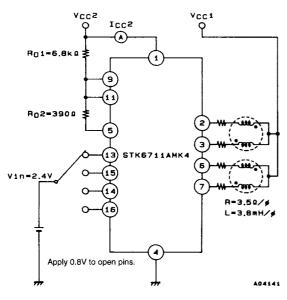
## **Test Circuit**

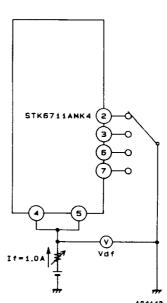




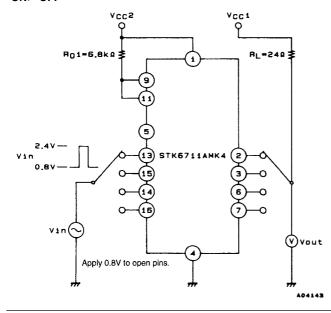


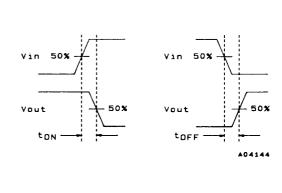
I<sub>CC</sub>2



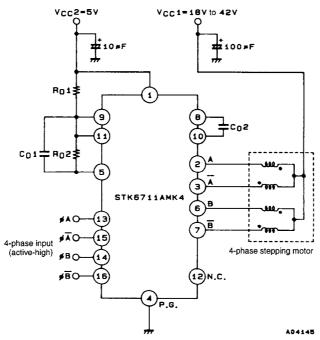


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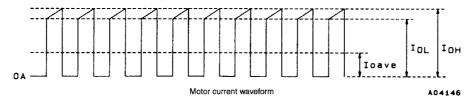


#### **Sample Application Circuit**



#### **Motor Current**

The following figure shows the motor current waveform when all 4-phase inputs are locked.



The motor current is set by the Vref voltage which is determined by the resistors  $R_{O}1$  and  $R_{O}2$  connected to pins 9 and 11.  $I_{OH}$  and Vref are related as shown in the following equations, where K is a correction coefficient for actual measurement.

$$I_{OH} = K \times \frac{R_O 2}{R_O 1 + R_O 2} \times \frac{V_{CC} 2}{R7}$$

$$Vref = \frac{R_O 2}{R_O 1 + R_O 2} \times V_{CC} 2$$

 $K \approx 1.2$  and  $R7 = R8 \approx 0.33\Omega \pm 3\%$ 

Reference values are such that  $R_O1{=}6.8k\Omega$  and  $R_O2{=}390\Omega$  at  $I_{OH}\approx 1A.$ 

# Motor Hold Noise Countermeasures (C<sub>O</sub>1 and C<sub>O</sub>2 Capacitors)

During motor hold, there may be cases where the motor generates audible noise. In this case, capacitors  $C_O1 \approx 0.01 \mu F$  and  $C_O2 \approx 100$  to 200pF can be added to prevent this noise as shown in the Sample Application Circuit. During normal operation, however, these capacitors are not necessary.

#### **STK6711AMK4**

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